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L1	35	703/18.ccls. and @pd>="20071001"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2008/07/21 15:05
L2	6	("5745371" "6557154" "6571184" "6618834" "6631502" "6850878"). PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/07/21 15:09
L3	97	(noise near analysis) and (printed near circuit) and @ad<"20030601"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2008/07/21 15:12



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S Chun, M Swaminathan, LD Smith, J Srinivasan, Z ... - Advanced Packaging, IEEE Transactions on (see also ..., 2001 - ieeexplore.ieee.org

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... -domain (FDFD) algorithm for analysis of **switching noise** in **printed circuit** boards and packages - all 11 versions »

OM Ramahi, V Subramanian, B Archambeault - Advanced Packaging, IEEE Transactions on (see also ..., 2003 - ieeexplore.ieee.org

... Abstract—Simultaneous **switching noise** (SSN) compromises the integrity of the power distribution structure on multilayer **printed circuit** boards (PCB). ...

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Estimating the **noise** mitigation effect of local decoupling in **printed circuit** boards - all 7 versions »

J Fan, W Cui, JL Drewniak, TP Van Doren, JL ... - Advanced Packaging, IEEE Transactions on (see also ..., 2002 - ieeexplore.ieee.org

... I. I INTRODUCTION H IGH-FREQUENCY **noise** in **printed circuit** boards (PCBs) results from both simultaneous **switching** of digital logic within the core, as well as ...

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... bandgap structures for suppression and isolation of the parallel-plate **noise** in high-speed circuits - all 6 versions »

R Abhari, GV Eleftheriades - Microwave Theory and Techniques, IEEE Transactions on, 2003 - ieeexplore.ieee.org

... vias in parallel-plate environments [eg, multilayer **printed circuit** boards (PCBs ... as power/ground **noise**, ground bounce, simultaneous **switching noise** (SSN), etc. ...

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Power-bus decoupling with embedded capacitance in **printed circuit** board design - all 4 versions »

M Xu, TH Hubing, J Chen, TP Van Doren, JL Drewniak ... - Electromagnetic Compatibility, IEEE Transactions on, 2003 - ieeexplore.ieee.org

... for reducing power-bus **noise** in high-speed **printed circuit** board designs. ... bus **noise** (delta-I **noise**, ground bounce **noise**, simultaneous **switch noise**), power plane ...

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... In **printed circuit** boards (PCB), each digital gate ... source and connecting the **island** (isolated active ... The trench prevents the **switching noise** from propagating ...

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Power bus isolation using power islands in printed circuit boards - all 2 versions »

J Chen, TH Hubing, TP Van Doren, RE DuBroff - Electromagnetic Compatibility, IEEE Transactions on, 2002 - ieeexplore.ieee.org

... I. I INTRODUCTION Power bus **noise** caused by the **switching** of integrated ... is often desirable to isolate relatively noisy regions of a **printed circuit** board (PCB ...

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W Cui, J Fan, Y Ren, H Shi, JL Drewniak, RE ... - IEEE TRANSACTIONS ON ELECTROMAGNETIC COMPATIBILITY, 2003 - ieeexplore.ieee.org

... bus **noise** isolation in multilayer **printed circuit** board (PCB ... an effective method for minimizing **noise** propaga- tion ... provide power to some fast **switching** or noisy ...

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DC power bus noise isolation with power islands

W Cui, J Fan, H Shi, JL Drewniak - Electromagnetic Compatibility, 2001. EMC. 2001 IEEE ..., 2001 - ieeexplore.ieee.org

... power planes are often used for DC power bus **noise** isolation in multi-layered **printed circuit** board (PCB ... power bus **noise** results from **switching** of IC ...

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Modeling and Analysis of Power Distribution Networks for Gigabit Applications - all 5 versions

»

J Choi, SH Min, JH Kim, M Swaminathan, WW Beyene, ... - 2003 - doi.ieeecomputersociety.org

... simultaneously **switching** circuits in a **printed circuit** board (PCB ... thousands of drivers that **switch** simultaneously within ... filter any power supply **noise**, as shown ...

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Electromagnetic radiation and simultaneous switching noise in aCMOS device packaging

T Sudo, Y Ko, S Sakaguchi, T Tokumaru - Electronic Components and Technology Conference, 2000. 2000 ..., 2000 - ieeexplore.ieee.org

... Is found that the powerground **island** with decoupling ... and Simulation of Simultaneous **Switching Noise**", IEEE Trans ... in a digital **printed circuit** board", IEEE ...

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... the connection of the power island and other power ... and J. L. Prince, Simultaneous

Switching Noise of CMOS ... H. Shi, "Study of **printed circuit** board power bus de ...

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Progress in the methodologies for the electrical modeling of interconnects and electronic packages - all 3 versions »

AE Ruehli, AC Cangellaris - Proceedings of the IEEE, 2001 - [ieeexplore.ieee.org](#)

... distribution circuit in multilayered **printed circuit** boards, to ... delay, crosstalk, and simultaneous **switching noise** can be performed using reduced PEEC models ...

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[book] Pcb Design for Real-World Emi Control

B Archambeault - 2002 - [books.google.com](#)

... Introduction to EMI/EMC Design for **Printed Circuit** Boards / 3 ... energy can induce di/dt noise in circuits, causing false logic **switching** and improper ...

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Switching Noise of CMOS ... H. Shi, "Study of **printed circuit** board power bus de ...

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JG Yook, V Chandramouli, LPB Katehi, KA Sakallah, ... - Components, Packaging, and Manufacturing Technology, Part A, ..., 1997 - [ieeexplore.ieee.org](#)

... Terms—Circuit simulation, decoupling capacitors, elec- tromagnetic modeling, ground bounce, **printed circuit** board, tiling, simultaneous **switching noise**. ...

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Power bus decoupling on multilayer printed circuit boards - all 2 versions »

TH Hubing, JL Drewniak, TP Van Doren, DM Hockanson - Electromagnetic Compatibility, IEEE Transactions on, 1995 - [ieeexplore.ieee.org](#)

... technique to the analysis of delta-I **noise** on integrated ... ground planes of the **printed circuit** board. ... When a **switching** device draws additional current from the ...

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Reducing simultaneous switching noise and EMI on ground/powerplanes by dissipative edge termination - all 7 versions »

I Novak, SM Inc, MA Burlington - Advanced Packaging, IEEE Transactions on [see also ..., 1999 - [ieeexplore.ieee.org](#)

... [17] P. Wetmore, F. Chua, and X. Yuan, "Modeling simultaneous **switching noise** in high speed multilayer packages and **printed circuit** boards," in Proc. ...

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Simultaneous switch noise and power plane bounce for CMOS technology - all 5 versions »

L Smith, SM Inc, P Alto - Electrical Performance of Electronic Packaging, 1999, 1999 - [ieeexplore.ieee.org](#)

... **circuit** board (PCB). ... of simulta- neously **switching** drivers inside an electronic package mounted on a PCB. The simu- ... SSN **noise** is due to power plane bounce. The ...

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S Chun, M Swaminathan, LD Smith, J Srinivasan, Z ... - Advanced Packaging, IEEE Transactions on [see also ..., 2001 - [ieeexplore.ieee.org](#)

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HH Chen, JS Neely, IBMTJWR Center, Y Heights - Components, Packaging, and Manufacturing Technology, Part B: ..., 1998 - [ieeexplore.ieee.org](#)

... and achieve the desired resolution for a full-chip **switching noise** analysis, we